



Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

M.S.	O.I.P.E.	PATENT DATE
SCANNED	MI	Q.A.

APPLICATION NO. 09/922742	CONT/PRIOR F	CLASS 711	SUBCLASS 167	ART UNIT 2185 2186	EXAMINER M. Kim
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APPLICANTS

Toshikazu Nakamura

TITLE

Clock synchronized dynamic memory and clock synchronized integrated circuit

PTO-2040
12/89

ISSUING CLASSIFICATION

ORIGINAL						CROSS REFERENCE(S)									
CLASS			SUBCLASS			CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION															

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
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